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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Mitsushi FUJIKI**

Group Art Unit: **2823**

Application Number: **10/772,253**

Examiner: **Thanh V. Pham**

Filed: **February 6, 2004**

Confirmation Number: **6491**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE
INCLUDING A LOW-TEMPERATURE LOWER ELECTRODE-
FORMING STEP**

Attorney Docket Number: **042068**

Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

June 19, 2006

Sir:

In response to the Office Action dated March 31, 2006, Applicants amend the claims as follows and submit the following remarks.

Remarks begin on page 2 of this paper.